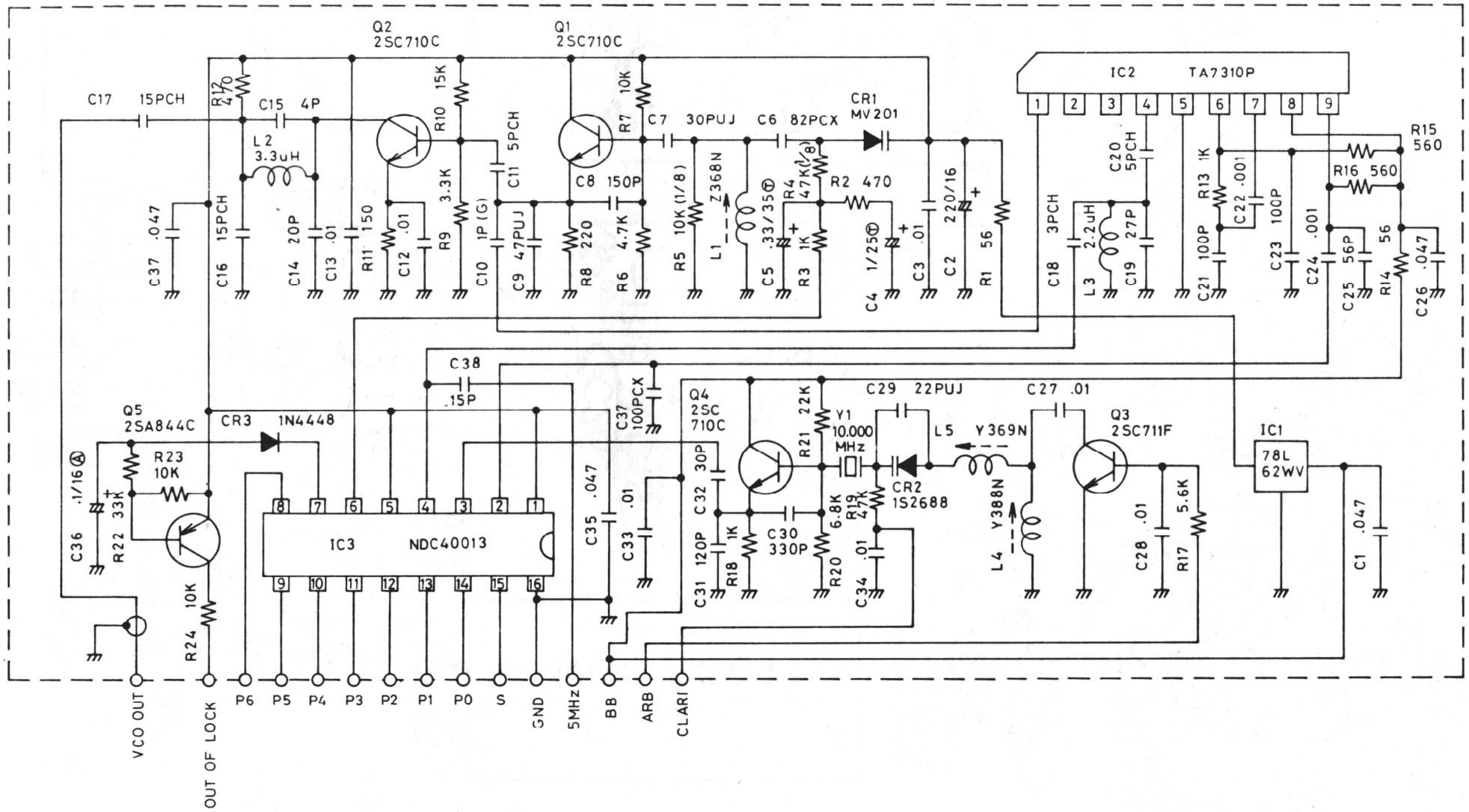PLL SCHEMATIC DIAGRAM